



CERAMIC DISC CAPACITOR SAFETY RECOGNIZED, AC SERIES(pitch5mm)

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PRODUCT SPECIFICATION

PRODUCT: CERAMIC DISC CAPACITOR SAFETY RECOGNIZED For PITCH: 5mm

	TYPE: AC SERIES
C	USTOMER:
	DOC. NO.: POE-D12-00-E-16
	APPROVED BY CUSTOMER

VENDOR:	
☐ HITANO ENTERPRISE CORP.	
7F-7, NO.3, WUCHUAN 1ST ROAD, NEW TAIPEI INDUSTRIAL PARK, NEW TAIPEI CITY, TAIWAN, R.O.C. TEL:+886222991331(REP.) FAX:+886222982466	ENTERPRISE CORP. ®
MAKER: PAN OVERSEAS (GUANGZHOU) ELECTRONIC CO.,LTD. NO.277,HONG MING ROAD,EASTERN SECTION, GUANG ZHOU ECONOMIC AND TECHNOLOGY DEVELOPMENT ZONE,CHINA	POElectronic POE



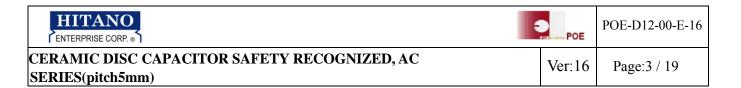
CERAMIC DISC CAPACITOR SAFETY RECOGNIZED, AC SERIES(pitch5mm)

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Record of change

Date	Version	Description	page
2008.6.3	1	1. D24-00-E-01(before) \rightarrow POE-D12-00-E-01(1 st edition)	FB.
2008.8.22	2	1 Complete lead code	19
		2. Add last SAP code "H" for halogen and Pb free, epoxy resin	3
2008.12.12	3	1. Complete the 13 th to 17 th codes of SAP P/N.	4
		2. Page layout adjustment.	
2009.7.16	4	 Complete Marking statement. Revised standard NO. of SEV, SEMKO, FIMKO, NEMKO, DEMKO and KEMA. Revised recognized NO. of FIMKO, NEMKO, DEMKO, KEMA and CQC. Down sizes. 	9 11
2009.9.14	5	1. "Protrusion length": "1.0max" revised to "2.0max (Or the end of lead	6 8
2007.7.14	3	wire may be inside the tape.)"	O
2009.12.24	6	 Delete the P/N of diameter above 10 mm. Marking 	6 9
		 3. Correct recognized No 4. Revised the Figure of impulse voltage test(Item 7.3.14) according to the standard IEC 60384-14 ed.3 	10 14
2011/1/13	7	1. Review SAP P/N about diameter code:	6
2011/1/13	,	2. Delete "AT" taping type.	4,5,8
		3. Add test item "Temperature Cycle".	15
		4. Add item 10 "Drawing of internal structure and material list"	20
2011/5/13	8	1. Add "1AC" type;	4
2011/3/13		2. Define the marking of the type "0AC" and "1AC";	9
		3. Delete "old P/N"	6~8
		4. Review the "Standard No. & Subclass & W.V. & Recognized No".	10
2012/2/7	9	 Review the "Standard No. & Subclass & W.V. & Recognized No". Review the "Operating Temperature Range" from "-25 to +125°C" to "-40 to +125°C" 	9
		3. Review the temperature of Step 1 from "-25+0/-3" to "-40+0/-3"	14
2012/4/6	10	1. In order to improve the traceability of the product, change the date	8
		code on capacitor body, new date code can trace back to production "Lot No."	
2013/5/6	11	 Review the Lead diameter φ from 0.60 +0.1/-0.05mm to 0.55+/-0.05mm In order the customer to know the round time of manufacture, review the date code on capacitor body, new date code can know the month of 	5,6,7
		manufacture. 3. Delete "No marked with " _" stand for Pb free". Add "epoxy resin"	8
		4. Review the Solderability time from 2±0.5s to 5±0.5s	11
		1. Review the "Manufactured Date" to "Products ID" on the marking page	8
		2. Delete "The marking can be printed on either one side or two side of coating	
2013/10/16	12	body and add, for SAP part number 10-11 digits ≤ '07' products' to two sides and, for SAP part number 11-12 digits ≥ '08' products' to one side.	8



Record of change (continue)

record of change (continue)				
Date	Version	Description		
2014/11/5	13	Review the terminal position of the lead wire.	7 8	
2016/1/27	14	 Review the Available lead code of Lead Configuration Revised standard NO. of VDE, SEV, SEMKO, FIMKO, NEMKO and DEMKO. 	5 9	
2016/5/3	15	1. Delete 6 pF~10 pF for P/N CH*AC***D06ōō, 12 pF~15 pF for P/N CH*AC120J06**,18 pF~24 pF for P/N CH*AC***J07**, 27 pF~33 pF for P/N CH*AC***J08**, and 36 pF~39 pF for P/N CH*AC***J09**.	6	
2016/11/3	16	1. Delete "CH" series.	4,6,10~14,19	

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1. Part number for SAP system

(Ex.) <u>YV</u> 0AC<u>472</u> <u>10</u> L <u>20</u> <u>5</u> <u>B</u> **(1) (4)** (5) (6) (7) (8) (9) (10) (11)(2) (3)

(1)Temperature characteristic (identified code)

CODE	SL	YP (Y5P)	YV(Y5V)	YU (Y5U)
Cap. Change	-1000~+350PPM/°C (+20°C~+85°C)	±10%	-80% ~ +30%	-55% to +20%

- (2)TYPE (identified by 3-figure code): 0AC = X1:400Vac/Y2:250Vac
- (3) Capacitance (identified by 3-figure code): EX.221=220pF
- (4)Capacitance tolerance (identified by code) C:±0.25pF,D:±0.5pF,J:±5%,K:±10%,M:±20% (5)Nominal body diameter dimension (identified by 2-figure code) : 06--Dmax7.0mm, 07--Dmax8.0mm... (6)Internal code: 0--Normal, other code--Special control
- (7)Lead Style Refer to "2. Mechanical".

(8) Packing mode and lead length (identified by 2-figure code)

Taping Code	Description
AN	Ammo box and product pitch: 12.7mm
AM	Ammo box and product pitch: 25.4 mm

Bulk Code	Description	
03	Lead length 3.0mm	
3E	Lead length 3.5mm	
04	Lead length 4.0mm	
4E	Lead length 4.5mm	
20	Lead length 20.0mm	

(9) Tolerance of lead length

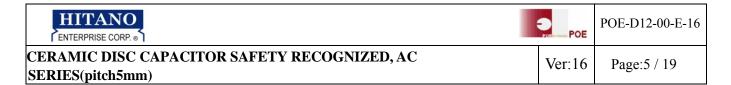
Code	Description		
A	±0.5 mm		
	(only for kink lead type)		
В	±1.0 mm		
С	Min.		
D	Taping special purpose		

(10)Lead space

Code Description		
5	5.0 ± 0.8 (For Bulk)	
3	5.0+0.8/-0.2mm(For Taping)	
Е	5.0 ± 0.5 mm	

(11)Epoxy resin code

Code	Description		
В	Pb free, Epoxy Resin		
Н	Halogen and Pb free, epoxy resin.		



2. Mechanical

Encapsulation: Epoxy resin, flammability UL94 V-0

Available lead code: (unit: mm)

Lead type	SAP P/N	Lead space	Lead Length	Packing	Lead Configuration	
	(13-17)digits	(F)	(L)			
	L03B5		3.0 ± 1.0		D max. T max.	
Lead style: L	L3EB5	5.0 ± 0.8	3.5 ± 1.0			
TypeL	L4EB5		4.5 ± 1.0	Bulk		
Straight long lead	L20C5		20 min.		Ø d	
Lead style: B	BAND5		Refer to "4.		D max.	
TypeB Straight long lead	BAMD5	5.0+0.8/-0.2	Taping format"	Tap. Ammo	e T F J L	
	D03A5		3.0 ± 0.5		D max. T max	
Lead style: D	D3EA5	5.0 ± 0.8	3.5 ± 0.5	Bulk		
	D04A5		4.0 ± 0.5			
Type D Vertical kink lead	DAND5		Refer to "4.		F T T T T T T T T T T T T T T T T T T T	
vertical kilk lead	DAMD5	5.0+0.8/-0.2	Taping format"	Tap. Ammo	Ø d+	
	X03A5		3.0 ± 0.5		D.may T.may	
	X3EA5	5.0 ± 0.8	3.5 ± 0.5	Bulk	D max.	
Lead style: X	X04A5		4.0 ± 0.5			
TypeX	XAND5				<u>*</u>	
Outside kink lead	XAMD5	5.0+0.8/-0.2	Refer to "4. Taping format"	Tap. Ammo	Signal of the second se	

^{*} Lead diameter Φd: 0.55 +/-0.05mm

^{*} C (Coating extension on leads): 3.0mmMax for straight lead lead style; Not exceed the kink for kink lead.





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3. Part numbering/T.C/Capacitance/ Tolerance/Diameter:

	,				Di	mensions	(unit: mm)	
SAP P/N	T.C.	Capacitance	Tolerance	D (max)	T (max)	Bulk type	F Taping type	φd
SL*AC***J060*		10,12,15,18,20,22,2 4,27,30,33, 36,39,47,50,51(pF)	±5%	7.0			•	
SL*AC***J070*	SL	56,62, 68,75(pF)	±5%	8.0				
SL*AC820J080*		82pF	±5%	9.0				
SL*AC101J090*		100pF	±5%	10.0				
YP*AC101K060*		100 pF	±10%	7.0				
YP*AC151K060*		150 pF	±10%	7.0				
YP*AC221K060*		220 pF	±10%	7.0			5.0+0.8/-0.2	
YP*AC331K060*		330 pF	±10%	7.0			(AND5)	0.55±0.05
YP*AC471K060*	Y5P	470 pF	±10%	7.0				
YP*AC561K070*		560 pF	±10%	8.0				
YP*AC681K070*		680 pF	±10%	8.0				
YP*AC821K080*		820 pF	±10%	9.0				
YP*AC102K080*		1000 pF	±10%	9.0	5.0 5.0	5.0±0.8		
YU*AC102M060*		1000 pF	±20%	7.0		2.0—0.0		
YU*AC152M080*		1500pF	±20%	9.0				
YU*AC222M080*	Y5U	2200 pF	±20%	9.0				
YU*AC332M100*	130	3300 pF	±20%	11.0			5.010.0/.0.2	
YU*AC392M120*		3900 pF	±20%	13.0	1		5.0+0.8/-0.2 (AMD5)	
YU*AC472M120*	1	4700 pF	±20%	13.0	1		(AMD3)	
YV*AC102M060*		1000 pF	±20%	7.0	1			1
YV*AC152M060*	Y5V	1500 pF	±20%	7.0]		5.0+0.8/-0.2	
YV*AC222M060*	131	2200 pF	±20%	7.0]		(AND5)	
YV*AC332M080*	1	3300 pF	±20%	9.0	1			
YV*AC392M100*		3900 pF	±20%	11.0	1			
YV*AC472M100*		4700 pF	±20%	11.0	1		5.0+0.8/-0.2	
YV*AC682M120*		6800 pF	±20%	13.0]		(AMD5)	
YV*AC103M140*		10000 pF	±20%	15.0]			

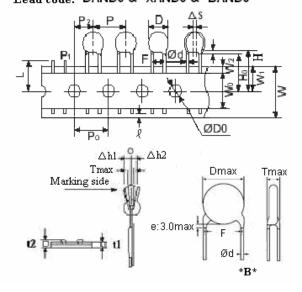
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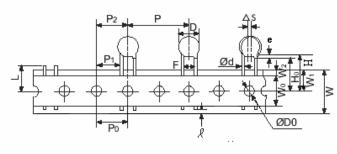
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4. Taping Format:

• 12.7mm pitch/lead spacing 5.0mm taping Lead code: *DAND5 & *XAND5 & *BAND5



■ 25.4mm pitch/lead spaceing 5.0mm taping Lead code: *DAMD5 & *XAMD5 & *BAMD5





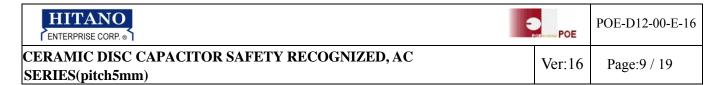
POE Part Number		*BAND5 *DAND5 *XAND5	*BAMD5 *DAMD5 *XAMD5	
Item	Symbol	Dimensions (mm)	Dimensions (mm)	
Pitch of component	P	12.7	25.4	
Pitch of sprocket	P0	12.7±0.3	12.7±0.3	
Lead spacing	F	5.0+0	.8/-0.2	
Length from hole center to component center	P2	6.35±1.5	12.7 ± 1.5	
Length from hole center to lead	P1	3.75±1.0	10.2±1.0	
Body diameter	D	See the "3. Part numbering/T.C/C	apacitance/ Tolerance/Diameter"	
Deviation along tape, left or right	ΔS	0±	2.0	
Carrier tape width	W	18.0 -	-1/-0.5	
Position of sprocket hole	W1	9.0±0.5		
Lead distance between the kink and center of sprocket hole	Н0	18.0+2.0/-0 (For: *DAND5 / *XAND5)	18.0+2.0/-0 (For: *DAMD5 / *XAMD5)	
Lead distance between the bottom of body and the center of sprocket hole	Н	20.0+1.5/-1.0 (For: *BAND5)	20.0+1.5/-1.0 (For: *BAMD5)	
Length from the terminal of the lead wire to the edge of carrier tape	l	2.0min (Or the end of lead wire m	ay be inside the hole-down tape.)	
Diameter of sprocket hole	D0	4.0	0±0.2	
Lead diameter	φd	0.55:	±0.05	
Total tape thickness	t1	0.6:	±0.3	
Total thickness, tape and lead wire	t2	1.5	max.	
Deviation across tape	∆h1/∆h2	2.0	max.	
Portion to cut in case of defect	L	11.0	max.	
Hole-down tape width	W0	8.0 min		
Hole-down tape distortion	W2	3.0 max.		
Coating extension on leads	e	e 3.0 max for straight lead style; Not exceed the kink leads for		
Body thickness	T	See the "3. Part numbering/T.C/Capacitance/ Tolerance/Diameter"		



5.Markingć

	KingC						
1.Type Desi	gnation	AC					
2.Nominal Capacitance Identified by 3-Figure Code. Ex. 47pF 1 = "47", 470pF 1 = "471"						"471"	
3.Capacitano	ce Tolerance	C:±0.25p	oF,D:±0.5pF,J:±5%,F	X:±10%,M:±20%			
4.Company	Name Code(Trade mar	k) K					
5.Manufactu	ired Date	Manufac 5:2015 6:2016 7:2017 : : : : : Mar C:F	6:2016 → Manufacture month:				
6.Approved 6.1 VDE	monogram: or	6.3 CSA 6.4 SEMKO	6.5 NEMKO		(F) 6.9 (coc 🔞	
6.2 UL	Туре	(for SAP	Two sides man	rking	One sid	le marking artnumber 11-12 08" products)	
Marking Ex.:	0AC (X1:400Vac/ Y2:250Vac)	AC47	1K DE 1:400V~ 2:250V~	\$\(\mathbb{N}\)\(\omega\)\	X1:400V~ Y2:250V~ 6C61234	FI S	

^{*&}quot;C", Marked with code "_" stand for Halogen and Pb free epoxy resin.

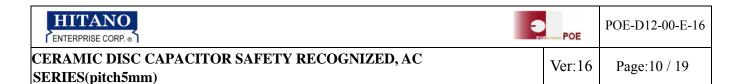


6. Scope

THIS SPECIFICATION APPLIES TO CERAMIC INSULATED CAPACITORS DISK TYPE USED IN ELECTRONIC EQUIPMENT.

- 1. VDE/SEV/SEMKO/FIMKO/NEMKO/DEMKO/UL/CSA recognized capacitor for Antenna coupling and AC line-by-pass.X1, Y2 Capacitor based on IEC 60384-14 "UL, CSA recognized for across-the-line, line-by-pass" and antenna-isolation
- 2. Approval Standard and Recognized No.

Safety Standard	Standard No.	Subclass	W.V.	Recognized No.
UL	ANSI/UL	X1	400VAC	E146544
OL	60384-14:2009	Y2	250VAC	E140344
CSA	CAN/CSA	X1	400VAC	2347969
CSA	E60384-14:2009	Y2	250VAC	2347909
VDE	EN 60384-14:2013	X1	400VAC	40001829
(ENEC)	IEC60384-14:2013	Y2	250VAC	40001829
SEV	IEC60384-14:2013	X1	400VAC	14.0554
SE V	IEC00364-14.2013	Y2	250VAC	14.0334
SEMKO	EN 60384-14:2013	X1	400VAC	1411212
SEMIKO	EN 00384-14.2013	Y2	250VAC	1411212
FIMKO	EN 60384-14:2013	X1	400VAC	NCS/FI 28679A1
THVICO	LIV 00304-14.2013	Y2	250VAC	1105/11/2007/711
NEMKO	EN 60384-14:2013	X1	400VAC	P14219060
IVEIVING	LIV 00304-14.2013	Y2	250VAC	114217000
DEMKO	EN 60384-14:2013	X1	400VAC	D-03994 A1
DEMICO	LIV 00304-14.2013	Y2	250VAC	D-03774 M1
CQC	GB/T 14472-1998	X1:40	0VAC /Y1:250VAC	CQC08001026519
KTL	K60384-14 2006	X1	400VAC or 440VAC	SU03065-14001
KIL	K00304-14 2000	Y2	250VAC	SU03065-14002



7. Specification and test method:

7.1 Operating Temperature Range:

-40 to +125°C

7.2 Test condition:

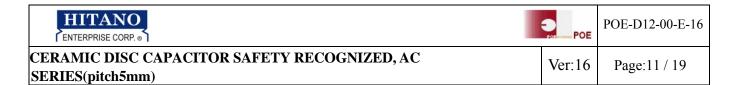
Test and measurement shall be made at the standard condition. (temperature 15~35℃, relative humidity 45~75% and atmospheric pressure 860~1060hpa). Unless otherwise specified herein.

If doubt occurred on the value of measurement, and measurement was requested by customer capacitors shall be measured at the reference condition. (temperature $20\pm2^{\circ}\text{Cor}25\pm2^{\circ}\text{C}$, relative humidity $60\sim70\%$ and atmospheric pressure $860\sim1060\text{hpa}$.)

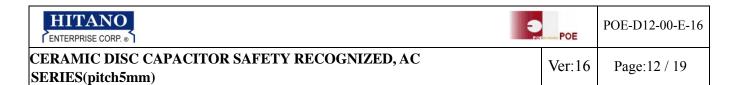
7.3 Performance:

	Item		Specification		Testing Method				
		Between lead wires		No failure.	The capacitors shall not be damage when AC2000V are applied between the lead wires for 60 sec. (Charge/Discharge current ≤ 50mA.)				
1	Dielectric Strength	Body Insulation		First the terminal of capacitor shall be connected together. The metal foil shall be closely wrapped around the body of the capacitor of about 3 to 4 mm from each terminal. Then the capacitor failure. No failure. No failure. No failure. No failure. No failure. Shall be inserted into a container filled with metal balls of about a container filled with metal balls of a container filled with metal balls.					
2	Insulation Resist	ance(I.R.)	10000N	MΩ min.	The insulation resistance shall be measured with 500±50VDC with 60±5sec. of charging.				
3	Capacitance		Within	specified tolerance					
			Char.	Specification	DOEGO TI				
	Dissination Fact	Dissipation Fortar(D.F.)		D.F≦2.5%	B&E&F: The capacitance shall be measured at 20±2°C with 1kHz±20% and 5V(rms.) or less.				
4	Dissipation Factor(D.F.) or Q		Y5V	D.F≦5.0%	SL: The capacitance shall be measured at 25°C with 1MHz±				
	V		SL	Q: 30pF&above:≥1000 Below 30PF:≥400+20×C	20% and1.0±0.2Vrms				
5	Temperature Ch	aracteristic	Char. Y5P Y5U Y5V SL	Capacitance Change Within ±10% Within -55 ~ +20% Within -80 ~ +30% -1000~+350ppm/°C (+20°C~+85°C)	The capacitance measurement shall be made at each step specified in table 1. (Table 1) Step Temperature 1 +20±2°C 2 -25±2°C 3 +20±2°C Pr-treatment: Capacitor shall be stored at 85±2°C for 1 hour. Then placed at room condition for 1(*)24±2 hours before measurement				
		Tensile	l l	ire shall not cut off or shall not be broken.	With the termination in its normal position the specimen is held by its body in such a manner that the axis of the termination is vertical: the tensile force of 10N shall be applied to the termination in the direction of its axis and acting in a direction away from the body of the specimen.				
6	Robustness of Termination Bending		Lead wire shall not cut off capacitor shall not be broken.		With the termination in its normal position the specimen is held by its body in such a manner that the axis of the termination is vertical: a mass applying a force of 5N is then suspended from the end of the termination. The body of the specimen is then inclined within a period of 2 to 3 sec., through an angle of approximately 90° in the vertical plane and then resumed to its initial position over the same period of time; this operation constitutes one bend. One bend immediately followed by a second bend in the opposite direction.				

^{* &}quot;room condition" temperature: 15~35°C, humidity: 45~75%, atmospheric pressure: 86~106kPa



Item			Specification	Testing Method
7	Solderability of leads		Lead wire should be soldered with uniform coating on the axial direction over 3/4 of the circumferential direction.	The lead wire of capacitor should be dipped into molten solder for 5 ± 0.5 sec. The depth of immersion is up to about 1.5 to 2.0 mm from the root of lead wires. Temp. of solder: Lead free solder (Sn-3Ag -0.5 Cu)
	Soldering Effect (Non-Preheat)			As shown in figure, the lead wires should be immersed in solder of 350 ± 10 °C or 260 ± 5 °C up to 1.5 to 2.0mm from the root of Terminal for 3.5 ± 0.5 sec (10 ± 1 sec for 260± 5 °C) Thermal Screen
8		Appearance I.R.	No marked defect. $1000 \mathrm{M}\Omega$ min.	First the capacitor should be stored at $120 + 0 / -5$ °C for $60 + 0 / -5$ sec. Then, as in figure, the lead wires should be immersed solder of $260 + / -5$ °C up to 1.5 to 2.0 mm from the root of terminal for 7.5 $+0 / -1$ sec.
	Soldering Effect (On-Preheat)	Dielectric Strength Capacitance	Per Item 1. Y5P,Y5U,Y5V6 Within ±10% SL: Within ±2.5%or ±0.25pF,Whichever is large.	Pre-treatment: Capacitor shall be stored at 85±2°C for 1hour then placed at *room condition for 24±2hours before initial measurements. Post-treatment: Capacitor shall be stored for 1 to 2hours at *1 room condition.



	Item		Specification	Testing Method
9	Humidity (Under Steady State)	Appearance Capacitance	No marked defect. Y5P: Within ±10% Y5U: Within ±20% Y5V: Within ±30% SL: Within±2.5%or ±0.25pF,Whichever is large. Y5P,Y5U65.0% max.	Set the capacitor for 500±12 hours at 40±2°C, in 90 to 95% humidity. Then capacitor shall be stored for 1 to 2 hours at room condition.
10	Humidity Loading	Q Q	Y5Vć7.5% max. SL: Less than 30pF=> $Q \ge 100+10 \times C/3$ More than 30pF=> $Q \ge 200$ Y5P,Y5U,Y5V3000M Ω min.	Apply the rated voltage for 500±12 hours at 40±2°C, in 90 to 95% humidity and set it for 1 to 2 hours at room condition.
		I.R.	SLĆ1000MΩ min.	Impulse Voltage:
		Appearance Capacitance	No marked defect. Y5P,Y5U,Y5V¢Within ±20% SL: Within±3%or ±0.3pF,Whichever is large.	Each individual capacitor shall be subjected to a 5kv impulses for three times. After the capacitors are applied to life test. $ \begin{array}{c ccccccccccccccccccccccccccccccccccc$
11	Life	I.R. Dielectric Strength	$3000M\Omega$ min. SL: $1000M\Omega$ min. Per Item 1.	Fig. The specimen capacitors are placed in a circulating air oven for a period of 1000 hrs. The air in the oven is maintained at a
				temperature of 125±2°C. Throughout the test. The capacitors are subjected to an AC425Vrms.(for 2AC type) or AC510Vrms.(for 3AC type) alternating voltage of mains frequency. Except that once each hour the voltage id increased to 1000Vrms for 0.1sec.
12	Flame Test	Cycle 1~4 5	me discharge as follows. Time 30 sec, max. 60 sec, max.	The capacitor shall subject to applied for 15 sec And then removed for 15 sec, until 5 cycles. Fig.
				(Unit: mm)

^{* &}quot;room condition" temperature: 15~35°C, humidity: 45~75%, atmospheric pressure: 86~106kPa



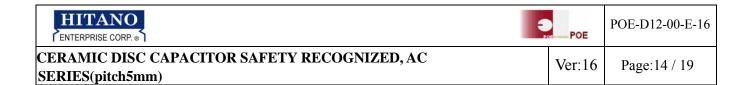


CERAMIC DISC CAPACITOR SAFETY RECOGNIZED, AC SERIES(pitch5mm)

Ver:16

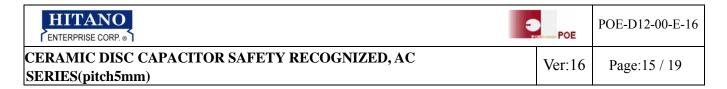
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	Item	Specification	Testing Method The specimens shall be individually wrapped in at least one but more then			
13	Active Flammability	The cheesecloth shall not be on fire.	two complete layers of cheesecloth. The specimens shall be subjected to 20 discharges. The interval between successive discharges shall be 5sec. The Uac shall be maintained for 2 min. after the last discharge. Fig. S1			
14	Passive Flammability	The burning time shall not be exceeded the time 30 sec. The tissue paper shall not ignite.	The capacitor under test shall be held in the flame in the position, which best promotes burning. Each specimen shall only be exposed once to the flame. Time of exposure to flame: 30 sec Length of flame: 12±1 mm Gas burner: Length 35 mm min. Inside Dia.:0.5±0.1 mm Outside Dia.:0.9 mm max. Gas Butane gas Purity 95% min. Fig. approximately 8 mm Test specimen Test specimen Tissue			



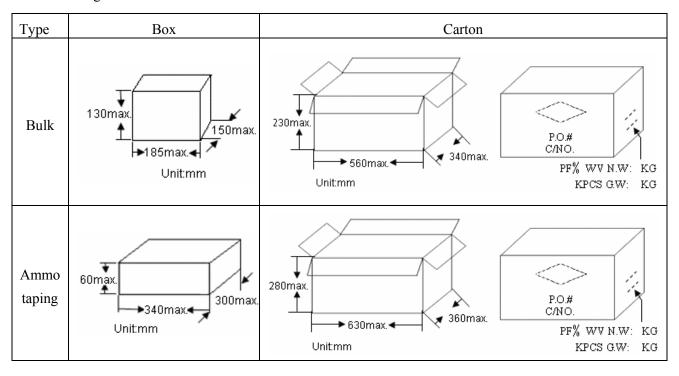
	Item Specification					Testing Method				
		Appearance	No marked defect	The capacitor	r shou	ld be subjected to 5	temperature c	cycles,		
		Char. Cap. Change	DF / Q	┨ .	<temperature 5="" cycle="" cycles="" time:=""></temperature>					
			Q≧275+5/2C (C <		Step	Temperature(°C)	Time(min)			
		SL ≦±5%	30pF) Q≥350 (C≥30pF)		1	-40+0/-3	30			
	T	Y5P ≦±10%	DF≦5.0%		2	Room temp.	3			
15	Temperature Cycle	Y5U, Y5V ≦±20%	DF≦7.5%		3	125+3/-0	30			
		I.R.	$3000 M\Omega$ min.	Due to et e	4	Room temp.	3			
		Dielectric strength	Per Item 1	Pre-treatment: Capacitor shall be stored at 85±2°C for 1hour.then at 1room condition for 24±2hours. Post-treatment:						
				condition.	Capacitor shall be stored for 1 to 2hours at ¹ room condition.					

^{*&}quot;room condition" temperature: 15~35°C, humidity: 45~75%, atmospheric pressure: 86~106kPa



8. Packaging Baggage:

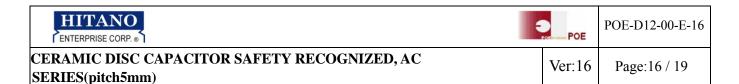
8.1 Packing size:



8.2 Packing quantity:

Packing type	The code of 14th to 15th in SAPP/N	MPQ(Kpcs/Box)
Taping	AN	1

Packing type	Lead length	Size code of 10th to 11th in SAPP/N	MPQ (Kpcs/Bag)	Kpcs/Box
	Longlead	06~12	0.5	1.5
	(L≧20mm)	13-15	0.5	1
Bulk	Short lead	Short lead 06~14		2
	(L < 20mm)	15	0.2	1
	All	16	0.2	1



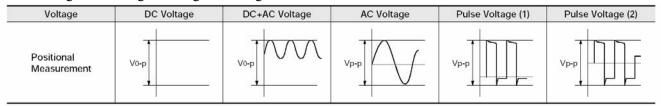
9. Notices:

9.1 Caution(Rating):

(1). Operating Voltage

Be sure to maintain the Vp-p value of the applied voltage or the Vo-p which contains DC bias within the rated voltage range.

When the voltage is started to apply to the circuit or it is stopped applying, the irregular voltage may be generated for a transit period because of resonance or switching. Be sure to use a capacitor within rated voltage containing this irregular voltage.



(2). Operating Temperature and Self-generated Heat

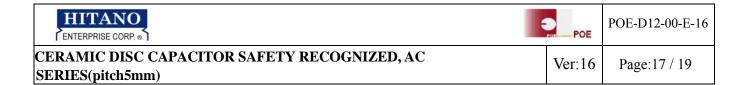
Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high-frequency current, pulse current or the like, it may have the self-generated heat due to dielectric-loss. Applied voltage should be the load such as self-generated heat is within 20°C on the condition of atmosphere temperature 25°C. When measuring, use a thermocouple of small thermal capacity-K of φ0.1mm and be in the condition where capacitor is not affected by radiant heat of other components and wind of surroundings. Excessive heat my lead to deterioration of the capacitor's characteristics and reliability.

(3). Test condition for withstanding Voltage

I. Test Equipment

Test equipment for AC withstanding voltage shall be used with the performance of the wave similar to 50/60 Hz sine waves.

If the distorted sine wave or over load exceeding the specified voltage value is applied, the defective may be caused.



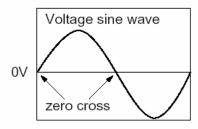
II. Voltage Applied Method

When the withstanding voltage is applied, capacitor's lead or terminal shall be firmly connected to the output of the withstanding voltage test equipment, and then the voltage shall be raised from near zero to the test voltage.

If the test voltage without the raise from near zero voltage would be applied directly to capacitor, test voltage should be applied with the *zero cross. At the end of the test time, the test voltage shall be reduced to near zero, and then capacitor's lead or terminal shall be taken off the output of the withstanding voltage test equipment.

If the test voltage without the raise from near zero voltage would be applied directly to capacitor, the surge voltage may arise, and therefore, the defective may be caused.

ZERO CROSS is the point where voltage sine wave pass 0V.- See the right figure.



(4). Fail-Safe

When capacitor would be broken, failure may result in a short circuit. Be sure to provide an appropriate fail-safe function like a fuse on your product if failure would follow an electric shock, fire or fume.

Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used.

9.2 Caution (Storage and operating condition):

Operating and storage environment

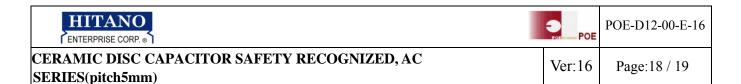
The insulating coating of capacitors does not form a perfect seal; therefore, do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. Before cleaning, bonding, or molding this product, verify that these processes do not affect product quality by testing the performance of a cleaned, bonded or molded product in the intended equipment. Store the capacitors where the temperature and relative humidity do not exceed –10 to 40 degrees centigrade and 15 to 85 % for 6 months maximum and use within the period after receiving the capacitors.

"Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used."

9.3 Caution (Soldering and Mounting):

9.3.1 Vibration and impact:

Do not expose a capacitor or its leads to excessive shock or vibration during use.



9.3.2 Soldering:

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could melt the internal junction solder and may result in thermal shocks that can crack the ceramic element.

When soldering capacitor with a soldering iron, it should be performed in following conditions.

Temperature of iron-tip: 400 degrees C. max.

Soldering iron wattage: 50W max.

Soldering time: 3.5 sec. max.

9.3.3 Cleaning (ultrasonic cleaning):

To perform ultrasonic cleaning, observe the following conditions.

Rinse bath capacity: Output of 20 watts per liter or less.

Rinsing time: 5 min maximum.

Do not vibrate the PCB/PWB directly.

Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires.

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9.4 Caution (Handling):

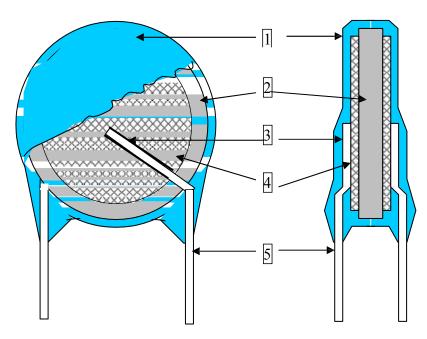
Vibration and impact

Do not expose a capacitor or its leads to excessive shock or vibration during use.

"Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used."

HITANO ENTERPRISE CORP. ®	3	POE	POE-D12-00-E-16
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10. Drawing of internal structure and material list:



Remarks:

No.	Part name	Material	Model/Type	Component
1	Insulation Coating	Epoxy polymer	1.EF-150	Epoxy resin, Pigment
			2.PCE-300	(Blue / UL 94 V-0)
2	Dielectric Element	Ceramic	SL/Y5P/Y5U/Y5V	BaTiO ₃
3	Solder	Tin-silver	Sn96.5-Ag3-Cu0.5	Sn96.5-Ag3-Cu0.5
4	Electrodes	Ag	1.SP-160PL	Silver, Glass frit
			2.SP-260PL	
5	Leads wire	Tinned copper clad	0.55±0.05 mm	Substrate metal: Fe & Cu
		steel wire		Surface plating: Sn 100%(3~7μm)